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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/719,998
	Filing Date	November 21, 2003
	First Named Inventor	Takashi Fujise
	Art Unit	2838
	Examiner Name	Jeffrey L. Sterrett
Total Number of Pages in This Submission	Attorney Docket Number	9319S-000579

ENCLOSURES (check all that apply)				
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name G. Gregory Schivley Bryant E. Wade	Reg. No. 27,382 40,344
Signature			
Date	November 23, 2005		

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EV 717 344 525 US



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/719,998
Filing Date: November 21, 2003
Applicant: Takashi Fujise
Group Art Unit: 2838
Examiner: Takashi Fujise
Title: STEP-DOWN CIRCUIT, POWER SUPPLY CIRCUIT, AND
SEMICONDUCTOR INTEGRATED CIRCUIT
Attorney Docket: 9319S-000579

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed August 25, 2005, please amend the application as follows and consider the remarks set forth below.

Remarks begin on page 2 of this paper.